

EXAMINER'S AMENDMENT

1. An examiner's amendment to the record appears below. Should the changes and/or additions be unacceptable to applicant, an amendment may be filed as provided by 37 CFR 1.312. To ensure consideration of such an amendment, it MUST be submitted no later than the payment of the issue fee.

The application has been amended as follows:

- a. Please amend the title to read: "Processing system including a reconfigurable channel infrastructure comprising a control chain with combination elements for each processing element and a programmable switch between each pair of neighboring processing elements for efficient clustering of processing elements".
2. The following is an examiner's statement of reasons for allowance.

With regard to claim 1, Appellant generally argues on page 6 that the Final Office Action's rejection of the presently pending claims is based upon a claim construction that expands the breadth of recited claim elements beyond reasonable claim scope boundaries inasmuch as the scope of the claim elements asserted in the Final Office Action is inconsistent with (1) Appellants' disclosure, (2) previous amendments and (3) Appellants' remarks, in previous Office Action responses, explaining the scope of the presently appealed claims. Appellant elaborates upon this arguments on pages 7-8, taking issue with the examiner's interpretation of the recited "control chain" and

"programmable switch" elements. Upon further consideration, in view of the above arguments, examiner has concluded that the arrangement and function of the NAND gates and their interconnection in Gove would not meet the aforementioned recited "control chain" and "programmable switch" elements. With regard to claim 29, examiner has likewise concluded that the arrangement and function of the NAND gates and their interconnection in Gove would not meet the concept of combining, by a combination element, an intermediate control signal with an operation control signal of a processing element and selectively passing the combined signal to a further processing element, in the specific context of the remaining limitations of the claim, as argued by Appellant in bullet point 3 on the bottom of page 11.

Any comments considered necessary by applicant must be submitted no later than the payment of the issue fee and, to avoid processing delays, should preferably accompany the issue fee. Such submissions should be clearly labeled "Comments on Statement of Reasons for Allowance."

Conclusion

3. Any inquiry concerning this communication or earlier communications from the examiner should be directed to KEITH VICARY whose telephone number is (571)270-1314. The examiner can normally be reached on Monday - Thursday, 7:00 a.m. - 5:30 p.m., EST.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Eddie Chan can be reached on 571-272-4162. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free). If you would like assistance from a USPTO Customer Service Representative or access to the automated information system, call 800-786-9199 (IN USA OR CANADA) or 571-272-1000.

/Eddie P Chan/
Supervisory Patent Examiner, Art Unit 2183

/Keith Vicary/
Examiner, Art Unit 2183